

DEC 12 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/759483	Docket Number	CNTR.2232
Filed	1/16/2004	Group Art Unit	2188
Examiner	MANO PADMANABHAN	Customer No.	23669
Application Title	MICROPROCESSOR WITH VARIABLE LATENCY STACK CACHE		
First Named Inventor	Rodney E. Hooker		

FACSIMILE COVER SHEET

To: Commissioner for Patents - Central Fax Number
Fax Number: 571-273-8300

From:
Fax Number: 512-233-0850

Pages: 28 (including this cover sheet)

Dear Sir:

Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,

By: /E. Alan Davis/
E. ALAN DAVIS
Registration No. 39,954
Tel: (512) 301-7234

Date: 12/12/2005

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Application Number	10/759483	Docket Number	CNTR.2232
Filed	1/16/2004	Group Art Unit	2188
Examiner	MANO PADMANABHAN	Customer No.	23689
Application Title	MICROPROCESSOR WITH VARIABLE LATENCY STACK CACHE		
First Named Inventor	RODNEY E. HOOKER		

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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Respectfully submitted,

By: /E. Alan Davis/

E. ALAN DAVIS
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Tel: (512) 301-7234

Date: 12/12/2005

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/759483
				Filing Date	1/16/2004
				First Named Inventor	Rodney E. Hooker
				Group Art Unit	2188
				Examiner Name	Mano Padmadabahn
Sheet	1	of	1	Attorney Docket Number	CNTR.2232

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2(If known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		5930820	07-27-1999	Thomas W. Lynch	
		6151661	11-21-2000	Adams et al.	
		20040186959	09-23-2004	Takuji Kawamoto	
		6415380	07-02-2002	Toshinori Sato	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ^{5 (If known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	AA	GB2307319	05-21-1997	Han Heung Kim		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	"Dual On-Chip Instruction Cache Organization in High Speed Processors." IBM Technical Disclosure Bulletin. IBM Corp. New York, US. Volume 37, No. 12, December 1994, pages 213-214.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

If you need assistance in completing this form, call 1-800-PTO-9199 (1-800-788-9199) and select option 2.